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**Sheet**

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of

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Application Number	09/589,621
Filing Date	06/07/2000
First Named Inventor:	Sara Ruhina Biyabani
Art Unit	2628
Examiner Name	Caschera, Antonio A.
Attorney Docket No.	04860.P2438

[illegible][illegible]

/Antonio Caschera/

Date  
Considered

04/26/2007

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<b>Substitute for Form 1449/PTO</b> <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	09/589,621
				Filing Date	06/07/2000
				First Named Inventor:	Sara Ruhina Biyabani
				Art Unit	2628
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Sheet	2	of	2	Attorney Docket No.	04860.P2438
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
/AC/		JUPITERMEDIA CORPORATION. "Integrated Video: Breaking the last PC Taboo," 802.11 Planet Conference & Expo World Tour, downloaded from the Internet at: <a href="http://www.cpuplanet.com/features/article.php/1566521">www.cpuplanet.com/features/article.php/1566521</a> , © 2003 Jupitermedia Corporation (2 pages).			
/AC/		KOC SIS, DAVID. "Frame-buffer wars: new directions in PC graphics," <u>EDN Access for Design, by Design</u> , May 9, 1996 (10 pages).			
/AC/		SILICON GRAPHICS, INC. "O <sup>2</sup> Unified Memory Architecture," <u>Silicon Graphics, Inc.</u> , 1997 (pp. 1-6).			
/AC/		WILLIAMS, IAN. "An Explanation of the Advantages Afforded by QED RM5200 and MIPS R12000 CPUs in O2," SGI White Paper, 1999 (pp. 1-41).			

Examiner Signature	/Antonio Caschera/	Date Considered	04/26/2007
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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